J_CLACK_BUS
09 18 540 6904
J_ZIF_ROPE
240-1280-00-0602J
J_PWR
Conn_01x04
J_RST
Conn_01x02
U_DEC_X1
U_DEC_X2
U_DEC_Y1
U_DEC_Y2
CD74HCT138E
U_TRX_LO
U_TRX_HI
CD74HCT245E
U_OP3
LM393AP
LM393/NOPB
SKT_X_LEFT
SKT_X_RIGHT
SKT_Y_TOP
SKT_Y_BOTTOM
PinSocket_2x16
D1
D2
D3
D4
D5
D6
D7
D8
D9
D10
D11
D12
D13
D14
D15
D16
D_TVS
1N5819
SMAJ5.0A
R1
R2
R3
R4
R5
R6
R7
R8
R9
R10
R11
R12
R13
R14
R15
R16
R17
R18
R19
R20
330R 1%
1k 1%
10k 1%
DSP_A0
DSP_A1
DSP_A2
DSP_A3
DSP_A4
DSP_A5
DSP_D0
DSP_D1
DSP_D2
DSP_D3
DSP_D4
DSP_D5
HDSP-7803
100n 50V
10u 16V
H1
H2
H3
H4
H5
H6
H7
H8
MountingHole_M3
PWR_FLAG

40-Pin CLACK-BUS • Antigravity AI
12x LED Displays & ZIF-40 Socket
175mA Coincident Matrix Drivers
Christian Buchnas • Peter Prikasky
CLACK-MAINFRAME

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Title: Board 02: Controller Baseboard

Size: A2	Date: 2026-11-07	**Rev: v1.0**
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