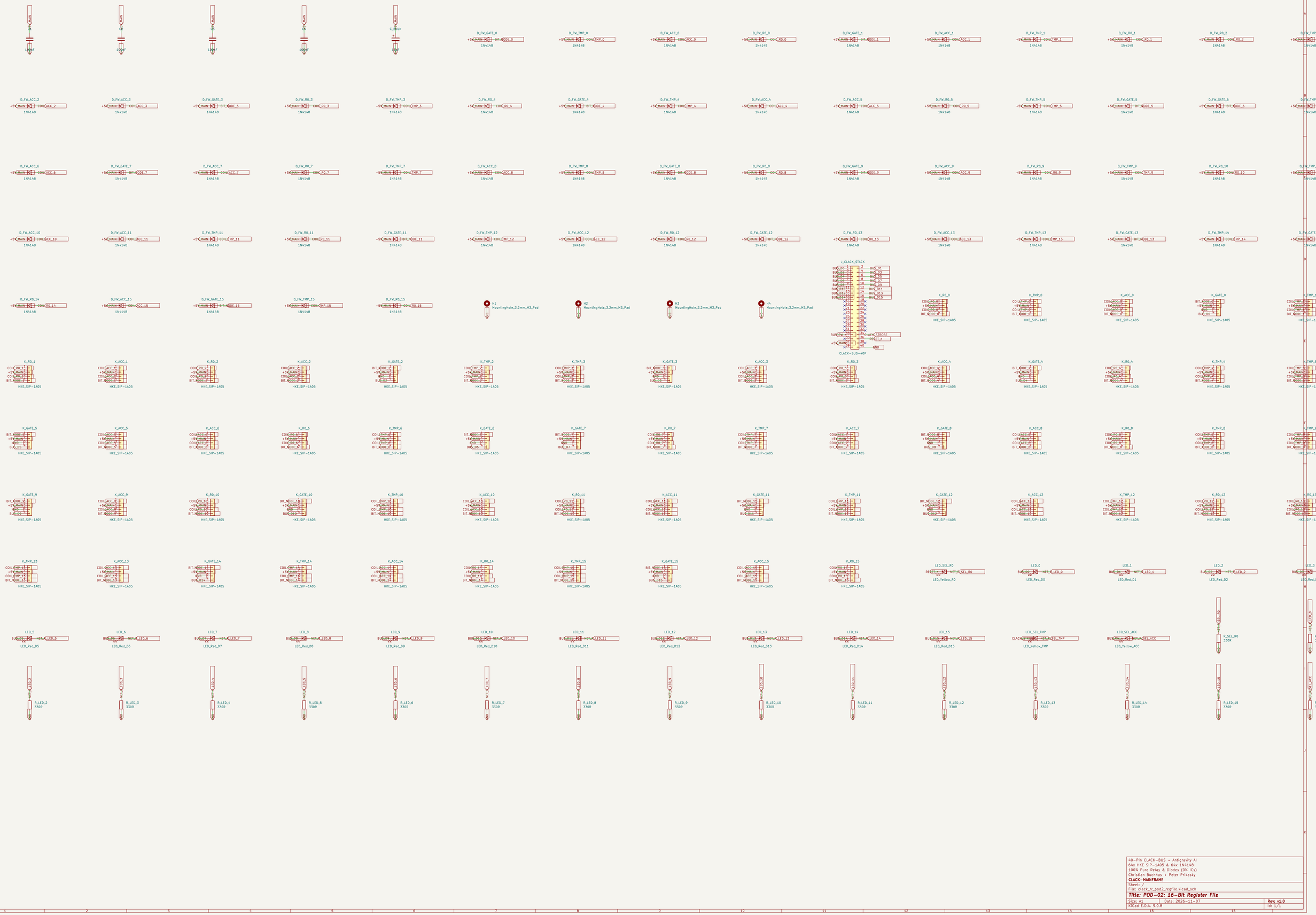


40-Pin CLACK-BUS • Antigravity AI
64x HKE SIP-1A05 & 64x 1N4148
100% Pure Relay & Diodes (0% ICs)
Christian Buchhas • Peter Prikasky
CLACK-MAINFRAME
Sheet: /
File: clack_rr_pod2_regfile.kicad_sch

Title: POD-02: 16-Bit Register File

Size: A1 Date: 2026-11-07	**Rev: v1.0**
KiCad E.D.A. 9.0.8	Id: 1/1