C_OSC1
100nF
C_OSC2
100nF
C_OSC3
100nF
C_MUX
100nF
C_RES_L_H
100nF
C_OPA_L
100nF
C_RES_H_H
100nF
C_RES_L_L
100nF
C_REC
100nF
C_STATUS
100nF
C_RES_H_L
100nF
C_OPB_L
100nF
C_OPA_H
100nF
C_OPB_H
100nF
H1
MountingHole_3.2mm_M3
H2
MountingHole_3.2mm_M3
H3
MountingHole_3.2mm_M3
H4
MountingHole_3.2mm_M3
J_CLACK_STACK
CLACK-BUS 2x20
K_STEP1
HKE_SIP-1A05
K_OSC1
HKE_SIP-1A05
K_OSC2
HKE_SIP-1A05
K_STEP2
HKE_SIP-1A05
K_STEP3
HKE_SIP-1A05
K_OSC3
HKE_SIP-1A05
K_STEP4
HKE_SIP-1A05
K_STEP5
HKE_SIP-1A05
K_STEP6
HKE_SIP-1A05
K_STEP7
HKE_SIP-1A05
K_STEP8
HKE_SIP-1A05
K_STEP9
HKE_SIP-1A05
K_STEP10
HKE_SIP-1A05
K_STEP11
HKE_SIP-1A05
K_STEP12
HKE_SIP-1A05
K_STEP13
HKE_SIP-1A05
K_STEP14
HKE_SIP-1A05
K_STEP15
HKE_SIP-1A05
K_STEP16
HKE_SIP-1A05
K_LOCK
HKE_SIP-1A05
LED_DIV0
LED
LED_S1
LED
LED_S2
LED
LED_S3
LED
LED_S4
LED
LED_S5
LED
LED_S6
LED
LED_S7
LED
LED_S8
LED
LED_S9
LED
LED_S10
LED
LED_S11
LED
LED_S12
LED
LED_S13
LED
LED_S14
LED
LED_S15
LED
LED_S16
LED
LED_CLK
LED_BUSY
LED
LED_READY
LED
R_DIV0
330R
R_LED_S1
330R
R_OSC1
1k
R_LED_S2
330R
R_OSC2
1k
R_LED_S3
330R
R_OSC3
1k
R_LED_S4
330R
R_LED_S5
330R
R_LED_S6
330R
R_LED_S7
330R
R_LED_S8
330R
R_LED_S9
330R
R_LED_S10
330R
R_LED_S11
330R
R_LED_S12
330R
R_LED_S13
330R
R_LED_S14
330R
R_LED_S15
330R
R_LED_S16
330R
R_BUSY
330R
R_READY
330R
R_CLK
330R
U_OPB_H
74HCT574
PWR_FLAG
PWR_FLAG
U_MUX
74HCT138
U_OPA_H
74HCT574
U_RES_H_L
74HCT574
U_RES_L_L
74HCT574
U_OPA_L
74HCT574
U_STATUS
74HCT541
U_RES_L_H
74HCT574
U_RES_H_H
74HCT574
U_OPB_L
74HCT574

40-Pin CLACK-BUS • Antigravity AI
16-Step Shift-and-Add Sequencer
Autonomous Hardware Multiply & Div
Chrislan Buchhas • Peter Prikasky
CLACK-MAINFRAME
Sheet: /
File: clack_rr_pod7_math.kicad_sch
Title: POD-07: Relay Math Coprocessor
Size: A1 | Date: 2026-11-07 | **Rev: v1.0**
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