## POD 05: EUROCARD 3U BACKPLANE, 40-PIN CLACK-BUS & POWER DISTRIBUTION

### SLOT 1 (3U EUROCARD)

**40-Pin Shrouded Header**

J_SLOT1 (2.54mm Grid)

**Bus Signal Assignment:**

Pins 1..16: D0..D15
Pins 17..32: A0..A15
Pin 33: BUS_RW_n
Pin 34: CLACK_STROBE
Pin 35: READY_WAIT
Pin 36: RESET_n
**Pin 37: +5V_MAIN (10A)**
**Pin 38: +3V3_LOGIC**
**Pin 39: +24V_IND**
**Pin 40: GND Ground**

**Bus Termination:**

10k Pull-Up Resistor Arrays
BAT43 Schottky Clamping
Suppresses reflections

### SLOT 2 (3U EUROCARD)

**40-Pin Shrouded Header**

J_SLOT2 (2.54mm Grid)

**Bus Signal Assignment:**

Pins 1..16: D0..D15
Pins 17..32: A0..A15
Pin 33: BUS_RW_n
Pin 34: CLACK_STROBE
Pin 35: READY_WAIT
Pin 36: RESET_n
**Pin 37: +5V_MAIN (10A)**
**Pin 38: +3V3_LOGIC**
**Pin 39: +24V_IND**
**Pin 40: GND Ground**

**Bus Termination:**

10k Pull-Up Resistor Arrays
BAT43 Schottky Clamping
Suppresses reflections

### SLOT 3 (3U EUROCARD)

**40-Pin Shrouded Header**

J_SLOT3 (2.54mm Grid)

**Bus Signal Assignment:**

Pins 1..16: D0..D15
Pins 17..32: A0..A15
Pin 33: BUS_RW_n
Pin 34: CLACK_STROBE
Pin 35: READY_WAIT
Pin 36: RESET_n
**Pin 37: +5V_MAIN (10A)**
**Pin 38: +3V3_LOGIC**
**Pin 39: +24V_IND**
**Pin 40: GND Ground**

**Bus Termination:**

10k Pull-Up Resistor Arrays
BAT43 Schottky Clamping
Suppresses reflections

### SLOT 4 (3U EUROCARD)

**40-Pin Shrouded Header**

J_SLOT4 (2.54mm Grid)

**Bus Signal Assignment:**

Pins 1..16: D0..D15
Pins 17..32: A0..A15
Pin 33: BUS_RW_n
Pin 34: CLACK_STROBE
Pin 35: READY_WAIT
Pin 36: RESET_n
**Pin 37: +5V_MAIN (10A)**
**Pin 38: +3V3_LOGIC**
**Pin 39: +24V_IND**
**Pin 40: GND Ground**

**Bus Termination:**

10k Pull-Up Resistor Arrays
BAT43 Schottky Clamping
Suppresses reflections

### SLOT 5 (3U EUROCARD)

**40-Pin Shrouded Header**

J_SLOT5 (2.54mm Grid)

**Bus Signal Assignment:**

Pins 1..16: D0..D15
Pins 17..32: A0..A15
Pin 33: BUS_RW_n
Pin 34: CLACK_STROBE
Pin 35: READY_WAIT
Pin 36: RESET_n
**Pin 37: +5V_MAIN (10A)**
**Pin 38: +3V3_LOGIC**
**Pin 39: +24V_IND**
**Pin 40: GND Ground**

**Bus Termination:**

10k Pull-Up Resistor Arrays
BAT43 Schottky Clamping
Suppresses reflections

### SLOT 6 (3U EUROCARD)

**40-Pin Shrouded Header**

J_SLOT6 (2.54mm Grid)

**Bus Signal Assignment:**

Pins 1..16: D0..D15
Pins 17..32: A0..A15
Pin 33: BUS_RW_n
Pin 34: CLACK_STROBE
Pin 35: READY_WAIT
Pin 36: RESET_n
**Pin 37: +5V_MAIN (10A)**
**Pin 38: +3V3_LOGIC**
**Pin 39: +24V_IND**
**Pin 40: GND Ground**

**Bus Termination:**

10k Pull-Up Resistor Arrays
BAT43 Schottky Clamping
Suppresses reflections

### MASTER POWER DISTRIBUTION & MULTI-TOWER INTERCONNECT (TB_POWER)

Heavy-copper pours (>10A) for uninterrupted multi-pod operation • 4-Pin Phoenix screw terminal 5.08mm
**Separated ground routing: Analog GND, Digital GND, and Industrial 24V GND with star-point topology**

**16-BIT RELAY MAINFRAME (CLACK-MAINFRAME)**
**POD 05: BACKPLANE & BUS INTERCONNECT**

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|---|---|---|
| Architecture & Hardware Engineering:<br>**Christian Buchhas • Antigravity**<br>Original CPU Concept: Peter Prikasky (relaiscomputer.at) | Document No.:<br>**CLACK-POD-05** | |
| Standard & Specification:<br>**Eurocard 3U • 40-Pin CLACK-BUS • relaycomputer.org** | Date:<br>**2026-11-04** | Rev / Sheet:<br>**v2.0 (1 / 1)** |